OpenCPI
AD9361 ADC Test App Guide (plutosdr platform)

OpenCPI Release: v2.4.7

Revision History

Revision	Description of Change	Date
v1.7	Initial release	04/2020

1 Description

This application is intended to perform a hardware-in-the-loop test of the ad9361_adc.hdl worker.

2 Hardware Portability

This application is specific to the `plutosdr` platform.

3 Execution

3.1 Prerequisites

The following must be true before application execution:

- The following assets are built and their build artifacts (FPGA bitstream file/shared object file) are contained within the directory list of the OCPI_LIBRARY_PATH environment variable.
  - for `plutosdr/adi_plutosdr0_31` HDL/RCC platforms:
    * `ad9361_1r1t_test_adc_asm/cnt_1rx_1tx_thruasm_txsrc_plutosdr` assembly/container
    * `ad9361_config_proxy.rcc` (from assets project)
    * `file_write.rcc` (from core project)
- The current directory is the applications/ad9361_adc_test directory.

3.2 Command(s)

```
./<target-dir>/ad9361_adc_test
```

4 Verification

An application exit status of 0 indicates success, and non-zero indicates failure.